

Application Serial No.: 10/613,372
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48. (Previously Presented) A method according to claim 47 wherein said processing step includes amplifying at least one of said at least two component signals.

49. (Previously Presented) A method according to claim 47 wherein said processing step includes phase modulating at least one of said at least two component signals.

50. (Canceled)

51. (Previously Presented) A method according to claim ⁴⁵~~50~~ wherein said deliberate predistortion is based on an interpolation of entries in said table. ←

52. (Previously Presented) A system according to claim 34 wherein said predistortion subsystem receives a replica of said system output signal.

53. (Previously Presented) A system according to claim 35 wherein said deliberate predistortion is dependent on differences between said input signal and said replica of said system output signal.

54. (Previously Presented) A system according to claim 44 wherein entries in said lookup table are periodically updated based on characteristics of a replica of said system output signal.

55. (Previously Presented) A system according to claim 44 wherein said deliberate predistortion is based on an interpolation of entries in said table.

56. (Previously Presented) A system according to claim 34 wherein said predistortion subsystem includes:

determining means for determining said deliberate predistortion;

adjustment means for applying said deliberate predistortion to said input signal;

update means for periodically updating said determining means based on said system output signal.

57. (Previously Presented) A system according to claim 56 wherein said adjustment means receives parameters of said deliberate predistortion from said determining means.

58. (Previously Presented) A method according to claim 45 further including the step of taking a difference between said input signal and a replica of said system output signal to determine said characteristics of said system output signal.

59. (Canceled)

60. (Currently Amended) An adaptive predistortion subsystem for use with a signal processing system which produces a system output signal, the predistortion subsystem comprising: